



2812

IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

*S. Green*  
#2  
2-5-02

PATENT APPLICATION

Inventor(S)	Thomas J. Krutsick	Case:	5
Serial No.	09/650604	Group Art Unit	Not Yet Assigned
Filing Date	08/30/00	Examiner	Not Yet Assigned
Title	Field Plated Resistor With Enhanced Routing Area Thereover		

THE COMMISSIONER OF PATENTS AND TRADEMARKS  
WASHINGTON, D. C. 20231  
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INFORMATION DISCLOSURE STATEMENT  
CERTIFICATION UNDER 37 CFR 1.97

In accordance with 37 CFR 1.97 (b), the enclosed Information Disclosure Statement, with attached reference (s), is submitted for consideration in the above-identified application.

**NO FEE REQUIRED.**

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Lucent Technologies Deposit Account No. 12-2325** as required to correct the error.

Respectfully,

By *David L. Smith*  
David L. Smith, Corporate Counsel  
Reg. No. 30592  
610-712-3784

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Lucent Technologies

Date: 10/30/00

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Information Disclosure Statement with attachment(s)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:  
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*Mary Ellen Davis* (Date of Deposit)  
Date

**INFORMATION DISCLOSURE STATEMENT**

CASE 5

SERIAL NO. 09/650604

APPLICANT:

Thomas J. Krutsick

FILING DATE: 08/30/00

GROUP:

Examiner: Initial box if reference is considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

**U. S. PATENT DOCUMENTS**

☐ Patent No. 3,694,719, Filed on November 27, 1970 and issued on September 26, 1972 to Arjun Nath Saxena et al., Class: 317/235.

☐ AB: Patent No. 5,886,383, filed on January 10, 1997 and issued on March 23, 1999 to Daniel M. Kinzer Class: 257/341.

ATT: References as noted above

Examiner:

Date:

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